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APPLICANT
Claus E. FURST

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U.S. PATENT DOCUMENTS

Ref. Desig.	Examiner's Initials	Document Number	Date	Name	Class/ Subclass	(If appropriate) Filing Date

FOREIGN PATENT DOCUMENTS

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

Ref. Desig.	Examiner's Initials	
		Peluso, Vincenzo et al. "A 900-mV Low-Power ΔΣ A/D Converter with 77-dB Dynamic Range." IEEE Journal of Solid-State Circuits, Vol. 33, No. 12, December 1998; pp. 1887-1897.
		Jorgenson, Ivan et al. "Design of a 3rd Order Micro Power Switched Current ΔΣ-Modulator." The Third International Conference on Electronics, Circuits and Systems, October 13-16, 1996, Vol. 2, pp. 948-951.
		Furst, Claus E. "Low Power/Low Voltage Interface Circuitry for Capacitive Sensors; Design, Optimization and Applications." Department of Information Technology, Technical University of Denmark. March 31, 1997; pp. 71-86 and pages 123-138.
		Furst, Claus E. "A High Resolution Switched Capacitor 1bit ΣΔ Modulator For Low-Voltage/Low-Power Applications.". Department of Informaiton Technology, Technical University of Denmark.) NO DATE LISTED

Examiner: _____   Date Considered: _____

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